

## United States Patent and Trademark Office



UNITED STATES DEPARTMENT OF COMMERCI United States Patent and Trademark Office Address: COMMISSIONER FOR PATENTS P.O. Box 1450 Alexandria, Virginia 22313-1450 www.uspto.gov

APPLICATION NO. FILING DATE		LING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/760,434	10/760,434 01/20/2004		Martin Alter	M-077P1	9211
	7590 10/06/2004		EXAMINER		
Eugene H. V	alet		POTTER, ROY KARL		
ValetPatents 314 10th Ave	South		ART UNIT	PAPER NUMBER	
Edmonds, W	A 98020	)	2822		

DATE MAILED: 10/06/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application	Application No.		Applicant(s)					
		10/760,43	4	ALTER ET AL.						
	Office Action Summary	Examiner		Art Unit						
		Roy K Pot		2822						
	The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply									
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.  - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.  - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).										
Status					•					
1)	Responsive to communication(s) filed on									
2a)□	This action is <b>FINAL</b> . 2b)⊠ Thi	is action is n	on-final.	•						
3)□	Since this application is in condition for allowa	ance except	for formal matters, pro	secution as to the	e merits is					
	closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.									
Disposition of Claims										
4)⊠	Claim(s) 1-13 is/are pending in the application	n.								
•	4a) Of the above claim(s) is/are withdrawn from consideration.									
5)□	Claim(s) is/are allowed.									
6)⊠	Claim(s) <u>1-3,5,6 and 8-13</u> is/are rejected.									
7)⊠	Claim(s) <u>4 and 7</u> is/are objected to.									
. 8)□	Claim(s) are subject to restriction and/	or election re	equirement.							
Applicati	ion Papers									
9)[	The specification is objected to by the Examin	ner.	•							
10)	10) The drawing(s) filed on is/are: a) accepted or b) objected to by the Examiner.									
	Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).									
	Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).									
11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.										
Priority under 35 U.S.C. § 119										
12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).										
a) All b) Some * c) None of:										
/-	1. ☐ Certified copies of the priority documents have been received.									
2. Certified copies of the priority documents have been received in Application No										
3. Copies of the certified copies of the priority documents have been received in this National Stage										
application from the International Bureau (PCT Rule 17.2(a)).										
* See the attached detailed Office action for a list of the certified copies not received.										
Attachment(s)  1) Notice of References Cited (PTO-892)  4) Interview Summary (PTO-413)										
2) Notice of Draftsperson's Patent Drawing Review (PTO-948)										
3) 🔲 Infori	mation Disclosure Statement(s) (PTO-1449 or PTO/SB/08 or No(s)/Mail Date	8)	5) Notice of Informal P 6) Other:	atent Application (PT	O-152)					

Application/Control Number: 10/760,434

Art Unit: 2822

## **DETAILED ACTION**

Claims 1 – 13 are pending.

## Claim Rejections - 35 USC § 102

The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless – (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims 1 - 3, 5 - 6, and 8 - 10 are rejected under 35 U.S.C. 102(b) as being anticipated by Jacobs.

Jacobs, U.S. Patent 5,055,097, discloses an extended integration semiconductor structure. As shown in Figure 2E, a first die 25a has at least one die pad. A second die 25b also has a die pad. An electrical interconnect 21 between the first and second die. As shown in Figure 8, redistribution of contacts 26 on a chip 25 is accomplished in the same layer.

## Claim Rejections - 35 USC § 103

The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.

Claims 1 – 3, 5 – 6, 8 - 10 are rejected under 35 U.S.C. 103(a) as being

unpatentable over Jacobs, U.S. Patent 5,055,907.

In regard to claims 11 – 13, the prior art does not teach or suggest the recited

integrated circuit die ship set, each specifically having bipolar components. However,

column 7, line 15 describes that the chips can be off the shelf. Bipolar components are

well-known off-the -shelf components and would have been obvious to one of ordinary

skill in the art at the time the invention was made.

Allowable Subject Matter

Claims 4 and 7 are objected to as being dependent upon a rejected base claim,

but would be allowable if rewritten in independent form including all of the limitations of

the base claim and any intervening claims.

The prior art does not teach or suggest the recited wafer-scale integrated circuit

device.

Conclusion

The prior art made of record and not relied upon is considered pertinent to

applicant's disclosure.

Jacobs, U.S. Patent No. 5,192,719, discloses a low cost lightweight, fast dense

reliable extend integration semiconductor structure.

Cole et al., U.S. Patent 5,527,741 discloses a circuit module.

Application/Control Number: 10/760,434

Art Unit: 2822

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Roy K Potter whose telephone number is 571 272 1842. The examiner can normally be reached on M-F.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Roy K Potter Primary Examiner Art Unit 2822